Vulkan Video Core APIs

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Tony Zlatinski, NVIDIA
tzlatinski@nvidia.com
Presentation outline

- General Vulkan Video concept and architecture
- Vulkan Video from HW perspective
- Vulkan Core and Video add-on objects
- Vulkan Video execution model
- Vulkan queue interactions and synchronization
- Application-specific usecases of Vulkan Video
Vulkan Video API design goals

- Cross-platform and vendor-neutral low-level HW state-less video codecs API
- Closer integration with the Graphics and Displays
- Lower-level synchronization allowing for lower processing latency
- Enables scalable applications targeting high-throughput servers to low-power, limited-resource embedded devices.
  - A better solution for parallel video stream processing with ability to distribute work among multiple CPU and HW codec cores
  - Lower processing execution overhead
  - Lower CPU/GPU/ASIC and memory resource utilization
Decoding operations

Compressed bitstream

Basic Configuration

decode

output

Decoded Picture

Encoding operations

Picture to be compressed

Basic Configuration

encode

output

Compressed bitstream
Vulkan Video Support

NVIDIA, Intel & AMD are the first IHVs to implement support for video extensions:

- NVIDIA: Windows and Linux **BETA drivers**
- Intel: Coming soon
- AMD: Windows **BETA driver**

The official example application for Vulkan Video Decode extensions is the open-source `vk_video_decode` sample from NVIDIA.

The very popular `gstreamer` (developed by Igalia) and `ffmpeg` (developed by Lynne) video processing frameworks and `RADV` and `ANV` open-source drivers are early adopters of Vulkan Video.
Intel will support Windows OS for Intel® Arc™ A-Series Graphics and Intel® Iris® Xe Graphics.
- 11th Gen Intel® Core™ processor family (Codename Tiger Lake, Rocket Lake)
- 12th Gen Intel® Core™ processor family (Codename Alder Lake)
- 13th Gen Intel® Core™ processor family (Codename Raptor Lake)
- Intel® Arc® Graphics family (Codename Alchemist)
- The ANV open-source driver, developed by David Airlie, will support h264 decode from Skylake to the Intel Gen13/Arc Graphics. Possibly, h.265 decode, as well.

AMD will support Vulkan Video decode on all RDNA™ architecture-based graphics in AMD Software: Adrenalin Edition™ drivers for Windows and Linux to be released later this year. Windows Beta driver is available now for RX 5000 series and RX 6000 series GPUs. The RADV open-source driver, developed by David Airlie, will support decode on AMD nav10 and above, and possibility of support back to Polaris.

NVIDIA will make Vulkan video decode available starting with Maxwell and later (the latest one being ADA) GPUs for Linux and Windows OS. Windows and Linux BETA drivers are currently available.
Contemporary Video Decoding / Encoding

**Decoding operations**

- **Compressed bitstream**
- **Reference Pictures**
- **Out of band parameters**
- **Basic Configuration**

- **decode**
- **Reconstructed Picture**
- **Decoded Picture**
- **Meta data (Motion vectors, etc.)**

**Encoding operations**

- **Compressed bitstream**
- **Reference Pictures**
- **Out of band parameters**
- **Picture to be compressed**
- **Basic Configuration**

- **encode**
- **Reconstructed Picture**
- **Encoded Picture**
- **Meta data (Motion vectors, etc.)**
Video decoder (or encoder) hardware viewpoint

Video HW processing block and context

HW data context

- Per video stream context data and configuration parameters
- Per (sub-) frame input data
- HW scratch memory save & restore context
- Per (sub-) frame output data

Input HW queue ➔ HW video processing ASIC and/or GPU ➔ HW events and result data
Video decoder with hardware viewpoint

Basic Configuration mutable and per video stream

Out of band parameters

Reference Pictures

Per (sub-)frame data

Compressed bitstream

Video HW processing block and context

Per video stream context data and conf. parameters

Per (sub-) frame input data

HW scratch memory save / restore context

Per (sub-) frame output data

Input HW queue

HW video processing ASIC and/or GPU

HW events and result data

Reconstructed Picture

Decoded Picture

Meta data (Motion vectors, etc.)
Vulkan Video decoder with hardware viewpoint

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Vulkan Video Core and Codec Extensions

- Vulkan Video Decode Core
  - Vulkan Video Decode h.264
  - Vulkan Video Decode VP9
  - Vulkan Video Decode AV1
  - In the development pipeline
- Vulkan Video Encode Core
  - Vulkan Video Encode h.264
  - Vulkan Video Encode AV1
  - Vulkan Video Encode h.265
- Provisional spec
Vulkan queue family types and device queues

Queue family types:
- VK_QUEUE_GRAPHICS_BIT: Graphics
- VK_QUEUE_COMPUTE_BIT: Compute
- VK_QUEUE_TRANSFER_BIT: Transfer
- VK_QUEUE_VIDEO_DECODE_BIT_KHR: Video Decode
- VK_QUEUE_VIDEO_ENCODE_BIT_KHR: Video Encode

VkQueueFamilyProperties::queueFlags

VkQueue
- Video Encode / Transfer / Compute 0
- Video Decode / Transfer / Compute 0
- Video Decode / Transfer 0

VkQueue
- Graphics / Compute / Present 0
- Compute / Transfer 0
- Compute / Transfer 1
- Video Decode / Transfer / Compute 0
- Video Decode / Transfer / Compute 1
- Video Decode / Transfer / Compute 2
- Video Encode / Transfer / Compute 0
How many instances of video HW blocks are available?

Use the `vkGetPhysicalDeviceQueueFamilyProperties2()` to query how many video HW instances does the implementation support.

Which queue family supports video?

`VkQueueFamilyProperties2::queueFamilyProperties.queueFlags` contains `VK_QUEUE_VIDEO_DECODE_BIT_KHR` for decode or `VK_QUEUE_VIDEO_ENCODE.Bit_KHR` for encode.

How many HW block instances are supported?

`VkQueueFamilyProperties2::queueFamilyProperties.queueCount`

When creating the device, specify the number of HW blocks required:

`VkDeviceQueueCreateInfo::queueCount = numOfHwBlocksRequired`
Video Session Object

- The VkVideoSessionKHR object stores (read-only) video configuration settings and the context associated with for a single video stream
  - One session object per video stream
- Required for any video decode or encode operations
  - Specifies the video profile and maximum parameters for the video stream
- A video session instance supports a single compression standard only
  - H.264, HEVC, VP9, AV1, etc.
- Video Session object maintains the device memory heaps
  - The application allocates and binds VkDeviceMemory objects to the Video Session object which uses it for its memory heaps
Vulkan Video Profiles

- The `VkVideoProfileInfoKHR` and `VkVideoProfileListInfoKHR` structures are containers of formats describing the compressed bitstream. Specifically, those structures describe:
  - `videoCodecOperation` - codec operations such as h264 encode, h265 encode, etc.
  - `chromaSubsampling` - YCBCr 4:4:4, 4:2:2, 4:2:0, 4:0:0 color subsampling mode
  - `lumaBitDepth` and `chromaBitDepth` describe luminance & chroma channel bit depth - 8, 10, 12-bit
  - `videoUsageHints` - transcoding, offline, streaming, etc.

- **Video Profile structures must be included** when obtaining device properties or creating Vulkan objects that will be used with Vulkan Video. A single profile instance of `VkVideoProfileInfoKHR` is also required as part of `VkVideoSessionCreateInfoKHR` when creating a video session with `vkCreateVideoSessionKHR()`.

- The `VkVideoProfileListInfoKHR` structure contains multiple profiles and is usually used for matching the decoder’s output with the encoder’s inputs when requiring video transcoding.

### Vulkan API

<table>
<thead>
<tr>
<th>Vulkan API</th>
<th>Vulkan Structure to Extend</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>vkGetPhysicalDeviceFormatProperties2</code></td>
<td><code>VkFormatProperties2</code></td>
</tr>
<tr>
<td><code>vkCreateImage</code></td>
<td><code>VkImageCreateInfo</code></td>
</tr>
<tr>
<td><code>vkCreateImageView</code></td>
<td><code>VkImageViewCreateInfo</code></td>
</tr>
<tr>
<td><code>vkCreateBuffer</code></td>
<td><code>VkBufferCreateInfo</code></td>
</tr>
<tr>
<td><code>vkCreateQueryPool</code></td>
<td><code>VkQueryPoolCreateInfo</code></td>
</tr>
</tbody>
</table>
Video Session Parameters Object

- The `VkVideoSessionParametersKHR` object contains processing parameters.
  - It must be created against and belongs to a Video Session object.

- Multiple video session parameters objects can be used to process a video stream.
  - A video session parameters object can apply to the entire video stream, or only a part of it.
  - Session Parameter object is provided with the `vkCmdBeginVideoCodingKHR` command and remains in effect until the next `vkCmdEndVideoCodingKHR` command.

- The API can add parameters to a video session parameters object.
  - Previously added parameters with a specific ID cannot be modified.
  - The API can clone all video parameters into a new Session Parameter object.
An example of a Set of HEVC Codec Parameters

Video Session Parameters Object

SPS parameters ID 0

VPS parameters ID 0

Other Parameters

Other Parameters

PPS parameters ID 0

PPS parameters ID 1

PPS parameters ID 2

PPS parameters ID N

Implementation-specific stream context (opaque)

PPS/SPS/VPS/VUI parameters in h.265
Recording and submitting the command buffers

Record separate command buffers per queue submission ...

... then submit the recorded command buffers to the decoder queue.

VkSemaphore(s) wait on and/or signal

compressed frames

VkBuffer

decoder frame

VkQueue Video

VkFence

VkCommandBuffer N

VkCommandBuffer N + 1

VkCommandBuffer N + 2

VkCommandBuffer N + 4

VkSemaphore(s) wait on and/or signal

VkFence

VkBuffer

decoder frame

VkQueue Video

VkFence

VkCommandBuffer N

VkCommandBuffer N + 1

VkCommandBuffer N + 2

VkCommandBuffer N + 4

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Utilizing the host CPU multi-processing power

Dispatch command buffer recording work to multiple threads ...

... then sort the frames in decode order and submit to the decoder queue.

Warning: Make sure to allocate separate command pools for each thread!
Video VkCommandBuffers Queue Submission

Regular Vulkan Queue Submit Sequence:
- One or more Recorded Vulkan Video Command Buffers can be submitted
- The command buffer sequences can be synchronized by binary or timeline semaphores
- The command buffer sequences can be synchronized with the host CPU or between the Vulkan queues via semaphores or a fence
VkCommandBuffers Video Recording Sequence

- All Vulkan Video command sequences start with `vkCmdBeginVideoCodingKHR` and end with `vkCmdEndVideoCodingKHR`.
- Multiple Video Start/End command sequences are supported.
- Implicit ordering guarantees also apply to the video and other commands that belong to the same command buffer.

![Diagram showing video recording sequence]

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VkCommandBuffers Recording Context Setup

- **vkCmdBeginVideoCodingKHR** via the **VkVideoBeginCodingInfoKHR** parameters establishes a context for the subsequent video decode and/or encode commands
- **vkCmdEndVideoCodingKHR** terminates the context established by the last **vkCmdBeginVideoCodingKHR**

![Diagram of VkVideoBeginCodingInfo](image-url)
Recording VkCommandBuffer Commands

Only commands for decode/encode, barriers/events/query and transfer operation are allowed between the `vkCmdBeginVideoCodingKHR` and `vkCmdEndVideoCodingKHR` commands.

- **Handle Global Memory Barriers**
  - `VkMemoryBarrier`

- **Handle Image Layouts Transition**
  - `vkCmdResetQueryPool`
  - `vkCmdBeginQuery`
  - `vkCmdEndQuery`

- **Handle buffer barriers for stream buffers with `VkBufferMemoryBarrier`**

- **Handle image barriers for input, output and DPB images and buffers with `VkImageMemoryBarrier`**
  - `vkCmdPipelineBarrier`

Set per frame or slice queries*
- `vkCmdResetQueryPool`
- `vkCmdBeginQuery`
- `vkCmdEndQuery`

Set frame events
- `vkCmdWaitEvents()`
- `vkCmdSetEvent()`
- `vkCmdResetEvent()`

**One or more Video Decode operations**
- `vkCmdDecodeVideoKHR`

**One or more Video Encode operations**
- `vkCmdEncodeVideoKHR`

**Transfer Operations**:  
- `vkCmdCopyBuffer`
- `vkCmdCopyImage`
- `vkCmdCopyBufferToImage`
- `vkCmdCopyImageToBuffer`

---

* Some implementations may NOT support video queries.

** Transfer operation are only allowed if the video decode and/or encode queue advertises transfer operations via the VK_QUEUE_TRANSFER_BIT
Video Decode/Encode DPB Picture Resources

**DPB** stand for Decoded Picture Buffer

Global metadata

- VkImage Opaque Metadata
- VkImage Metadata

Per-slot session metadata

- Picture Resource
- VkImageView
- VkImage
- VkDeviceMemory

Per-slot Device Memory

- Slot State
- Reference Picture Metadata

Common Device Memory

- Video Session Internal Device Memory Heap Implementation
- VkDeviceMemory

Externally visible objects

Implementation-specific data

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DPB Slot Management

- Allocating/Associating DPB reference slots with \textit{slotId}
  - Add entry with \textit{slotId} and associated \texttt{VkImageView} resource in the array of \texttt{VkVideoBeginCodingInfoKHR}::\texttt{pReferenceSlots} within the \texttt{vkCmdBeginVideoCodingKHR} command

- Making DPB reference slot with \textit{slotId} valid
  - Decode (\texttt{vkCmdDecodeVideoKHR}) or Encode (\texttt{vkCmdEncodeVideoKHR}) commands targeting \textit{slotId} within the \texttt{VkVideoDecodeInfoKHR}::\texttt{pSetupReferenceSlot}

- Invalidating DPB slot with \textit{slotId}
  - Replace association of the reference slot with \textit{slotId} with a different \texttt{VkImageView} resource
  - Decode or Encode commands targeting the reference slot with \textit{slotId} (with \texttt{VkVideoDecodeInfoKHR}::\texttt{pSetupReferenceSlot})
  - Reset the decoder/encoder session via \texttt{vkCmdControlVideoCodingKHR} with \texttt{VK_VIDEO_CODING_CONTROL_RESET_BIT_KHR} flag set.
  - Replace the content of the associated \texttt{VkImageView} resource or unbind the backing memory
  - Change the layout of the associated \texttt{VkImageView} resource to an incompatible to DPB layout
DPB Slot Management Example

Allocating slots with associated picture resources

Frames | B | 1 | 2 | 3 | 4 | 5 | 6 | 7 | B | 7 | 8 | 9 | 10 | 11 | 12
---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---
0 | | 1 | 1 | 1 | 1 | 1 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 |
1 | | 12 | 12 | 12 | 12 | 12 | 12 | 12 | 12 | 12 | 12 | 12 | 12 | 12 | 11 |
2 | | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 | 2 |
3 | | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 | 3 |
4 | | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 | 4 |
5 | | 5 | 5 | 5 | 5 | 5 | 5 | 5 | 5 | 5 | 5 | 5 | 5 | 5 | 5 |
6 | | 6 | 6 | 6 | 6 | 6 | 6 | 6 | 6 | 6 | 6 | 6 | 6 | 6 | 6 |

Reference setup

DPB slots

Begin Video

Begin/End Video Commands

How to deal with Gaps and “non-existent” references?

Note: Sparse may require a different queue family to submit, if the video queue families do not advertise sparse capabilities

Available DPB slot

Allocated Target

Reference

Current FB

Image and buffer Memory Barriers

New Begin/end sequence

Manage Image/buffer Sparse Bindings
Make sure to allocate separate command pools for each thread!
Special Image Layout Transitions

- **DPB image special handling**
  - DPB images implicitly transition to VK_IMAGE_LAYOUT_UNDEFINED when:
    - Image is used for the first time with a video session
    - Content size or other parameters change within a video session
    - VkVideoSession object is reset
    - DPB slot is assigned for the first time with the image view representing the image
  - DPB images underlying content should not be affected when:
    - Transitioning reference images from VK_IMAGE_LAYOUT_VIDEO_DECODE_DPB or VK_IMAGE_LAYOUT_VIDEO_DECODE_DST to Gfx/compute friendly layouts

- **Video Input images transition**
  - When the content size or parameters change encode input images implicitly transition from VK_IMAGE_LAYOUT_VIDEO_ENCODE_SRC to VK_IMAGE_LAYOUT_UNDEFINED or VK_IMAGE_LAYOUT_PREINITIALIZED.

*Please don’t forget to transition the images to the correct video layout when the content size change!*
Video Queries

- **Result Status Query (optional)**
  - Used to check whether a set of operations has been completed successfully
  - Type is VK_QUERY_RESULT_WITH_STATUS_BIT_KHR
  - Can be used with other than video queue families

- **Encode Bitstream Range Query**
  - Describes range of bytes written in the bitstream buffer by video encode commands
  - Type is VK_QUERY_TYPE_VIDEO_ENCODE_BITSTREAM_BUFFER_RANGE_KHR

- **Queries supported with Video**
  - Host side vkGetQueryPoolResults()

- **Queries not supported with Video**
  - Device side: vkCmdCopyQueryPoolResults()
Video Properties and Capabilities

- **Supported codecs for a particular Vulkan video queue**
  - Queried through VkVideoQueueFamilyProperties2KHR, chained to vkGetPhysicalDeviceQueueFamilyProperties() function

- **Supported video decode and encode capabilities**
  - Queried through vkGetPhysicalDeviceVideoCapabilitiesKHR() function

- **Supported video output, input and DPB image formats**
  - Enumerated through vkGetPhysicalDeviceVideoFormatPropertiesKHR() function
Output reconstructed pictures implementation variations

VkVideoDecodeCapabilitiesKHR::flags reported by the implementation indicates if the output and reconstructed picture coincide (use one image view) or separate image view resources must be used.

VK_VIDEO_DECODE_CAPABILITY_DPB_AND_OUTPUT_DISTINCT_BIT_KHR - separate images must be allocated used for output and reconstructed picture. However, when linear output is required or output in RGB color space is required then the same output image can be used.

VK_VIDEO_DECODE_CAPABILITY_DPB_AND_OUTPUT_COINCIDE_BIT_KHR – a single image must be used for output and reconstructed image. However, for images requiring linear output a separate output image may still be required, along with an inlined post-processing transfer command.
HW load balancing - HW block instance per stream or multiple streams

Use multiple HW instances for decoding (or encoding) to fully utilize the HW.

This exact setup works against a single or multiple instances of the HW instances.

When using a single instance, the HW will time-slice between the submitted work.

This setup is compatible with both, multi-processes multi-thread configuration.
HW load balancing - multiple HW blocks per video stream

Split the decode/encode workload between multiple HW blocks (at frame boundaries).

When using timeline semaphore only one semaphore is sufficient.

**vkGetDeviceQueue**(device, queueFamilyIndex, queueIndex, VkQueue);

Warning: This configuration may reduce performance if there is insufficient work for all HW instances to work on and there are dependencies between the submitted work. Key frames only or closed-GOP sequences don’t require synchronization. Also, one of the HW devices can already be used by other APIs or device instances.
The display must support YCbCr images for its swapchain and there must be a compatible format that is returned by both `vkGetPhysicalDeviceSurfaceFormats2KHR()` and `vkGetPhysicalDeviceVideoFormatPropertiesKHR()`.
Interaction with the graphics and display (WSI)
HW queues workload graph using Vulkan fences (CPU based synchronization, including semaphore host wait and query waits)

Decoder/Transfer

Graphics/Compute

Display

msec

0 1 2 3 4 5 6 7 8 9 10 11 12

CPU host submits

Decode work

Transfer work

Graphics and/or compute work

Display Device swap time

Queue and host processing submit latency

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HW queues workload graph using Vulkan semaphores

- **CPU host submits**
- **Decoder/Transfer**
- **CPU host submits**
- **Graphics/Compute**
- **CPU host submits**
- **Display**
  - **msec**: 8, 16, 24, 32, 40, 48, 56, 64, 72

Decode work

Transfer work

Graphics and/or compute work

Display Device swap time

Queue and host processing submit latency

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HW queues workload graph using Vulkan semaphores latency because of the video (IPB) GOP structure

Decoder/Transfer

Graphics/Compute

Display

CPU host submits

Decode work

Transfer work

Graphics and/or compute work

Display Device swap time

Queue and host processing submit latency

CPU host submits

msec

8 16 24 32 40 48 56 64 → 72
Vulkan video frame timing using NVIDIA Nsight Graphics and Systems profiler

Check out Optimizing Vulkan 1.3 Applications with Nsight Graphics and Nsight Systems and Nsight Systems - Vulkan Trace.

NVIDIA Nsight Developer Tools are a collection of debuggers, profilers, and optimizers that support performance tuning for applications using many graphics APIs, including Vulkan.

The NVIDIA Nsight Systems profiler will enable parsing of the specific workload of a Vulkan Video decoding queue, providing insights into processing bottlenecks within the context of the application.
Dealing with video present time

Decoder/Transfer

Graphics/Compute

CPU host submits

CPU host submits

CPU host submits

Display

msec

Video timeline

Decode work

Transfer work

Graphics and/or compute work

Display Device swap time

Queue and host processing submit latency

Expected video present markers

Expected video present markers

Video timeline

CPU host submits

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Present time extensions

Both `VK_GOOGLE_display_timing` and `VK_EXT_present_timing` extensions provide the following features:

- Provide information on how long is the gap between display refreshes in nanoseconds resolution

- When presenting using `vkQueuePresentKHR`, indicate that image should be displayed at particular time and/or rate. This feature is also known as “Present at time”.

- Provides an accurate timestamp when the image was actually flipped on screen, as well as report on how early it could have been presented

Unfortunately, these extensions are not yet widely supported.
Managing video present time using `VK_GOOGLE_display_timing` extension

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High dynamic range (HDR)

When using video content with HDR authoring, the application would be required to collect the meta data information from the video stream and apply the transformation data as a post-processing step utilizing compute or graphics-enabled queues.

Beyond the content authoring Vulkan Video supports 8, 10 and 12-bit HDR video formats.
Protected and encrypted compressed content (1)

Support for protected content using privileged protected service.
Support for encrypted content is provided by vendor-specific extensions that allow decrypting keys to be installed on the decoder. The encrypted stream data is sent straight to the implementation, which requires the decoder to decrypt and decode the data.
Optimizing Memory Usage

- **Supporting dynamic content stream resize:**
  - Create Sessions with the maximum parameters required for video content: max resolution, max number of DPB, etc. or
  - Create a new session when the content size were to change;
- **Allocate image and buffer resources on demand**
  - When the content requires those resources
- **Free image or buffer resources that are not required**
- **Strip the resources of their physical memory backing**
  - Using sparse memory binding if supported
- **Use the output of the decoded images directly by Vulkan graphics/compute and display processing pipelines**
- **Use the output of the Vulkan graphics/compute to be consumed directly by the encoder's input**
Vulkan Sparse Resources with Video

- 4k/8k and 10/12-bit video content requires significant memory resources for stream buffers and picture images
  - One frame can be bigger than 2 MB. Video session may require 3-8 input and/or output images and 4 to 16 references that requires hundreds of megabytes of memory

- IHVs are advised in supporting Vulkan Sparse binding for buffers and images for memory efficient resource management
  - Sparse Partially-Resident Buffers
  - Support for both Sparse Buffers with VK_BUFFER_CREATE_SPARSE_RESIDENCY_BIT enables for portions of the Vulkan buffers used for the input or output stream to be unmapped

- Sparse Partially-Resident Images
  - VK_IMAGE_CREATE_SPARSE_BINDING_BIT and VK_IMAGE_CREATE_SPARSE_RESIDENCY_BIT to support an efficient memory use during content resolution change
  - Use vkQueueBindSparse() before or after the queuing video commands
Reusing images without reallocation on decode size change

If Sparse Residency is supported this unused area may not use any physical memory.
Conclusions

- Vulkan Video extension is powerful cross-platform API

- It provides a ton of flexibility in building scalable performing and highly configurable video applications.

- The API is backed-up with implementation by major IHVs and open source.
References

- An Introduction to Vulkan Video
- Vulkan Specification, including Vulkan video extensions
- Vulkan Headers
- Validation Layer PR
- Vulkan SDK including Vulkan video support and validation layers
- Vulkan video samples
- Optimizing Vulkan 1.3 Applications with Nsight Graphics and Nsight Systems and Nsight Systems - Vulkan Trace.
- NVIDIA beta Vulkan drivers
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